

10/647069

COJ

UNITED STATES PATENT AND TRADEMARK OFFICE

In re U.S. Patent No. 6,897,472 B2)
MEARS ET AL.)

Issued: MAY 24, 2005)

For: SEMICONDUCTOR DEVICE INCLUDING)
MOSFET HAVING BAND-ENGINEERED)
SUPERLATTICE)

REQUEST FOR CERTIFICATE OF CORRECTION OF
APPLICANT'S MISTAKE UNDER RULE 1.323

Commissioner For Patents
P.O. Box 1450
Alexandria, VA 22313-1450

09/20/2005 HDEMESS1 00000033 6897472

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100.00 OP

Sir:

In accordance with the provisions of Rule 1.323 of the Rules of Practice, the Patent Office is respectfully requested to correct matters in the above-identified patent in accordance with the Certificate of Correction Form PTO-1050 attached hereto.

The Patent Office is requested to issue a Certificate of Correction on the above-referenced Patent Document, and to place such a Certificate of Correction in the file, so that such will appear on any copies of the patent which are ordered in the future.

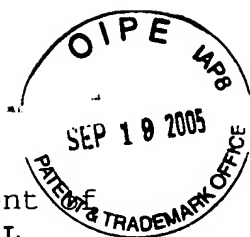
The corrections being made are of consequence to provide the proper recitation in the specification. Several of the mistakes occurred in good faith and are not the fault of the Patent and Trademark Office. However, some printing errors of the Patent Office are also being corrected. No new matter is added by these corrections.

Certificate

SEP 26 2005

of Correction

SEP 27 2005



In re Patent
MEARS ET AL.
Patent No. 6,897,472 B2
Issued: MAY 24, 2005

Authorization is given to charge the requisite fee as set forth in 37 CFR § 1.20(a) in the amount of \$100.00 to the credit card listed on the attached credit card payment form PTO-2038. The Commissioner is authorized to charge or credit any discrepancies in fee amounts to Deposit Account 01-0484.

Respectfully submitted,

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CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: COMMISSIONER FOR PATENTS, P.O. BOX 1450, ALEXANDRIA, VA 22313-1450, on this 16th day of September, 2005.

SEP 27 2005

UNITED STATES PATENT AND TRADEMARK OFFICE

CERTIFICATE OF CORRECTION

PATENT NO.	:	6,897,472 B2
DATED	:	May 24, 2005
INVENTOR(S)	:	Mears et al.

It is certified that error appears in the above-identified patent and that said Letters Patent are hereby corrected as shown below

Column 1, Lines 12-13	Delete: "and U.S. patent application Ser. No. 10/603,621"
Column 1, Line 45	Delete: "or a binary" Insert: --or binary--
Column 2, Line 4	Delete: "electromuminescence" Insert: --electroluminescence--
Column 7, Line 21	Delete: "25" Insert: --25'--
Column 8, Line 4	Delete: "from the both" Insert: --from both--
Column 9, Line 63	Delete: "also formed" Insert: --also be formed--
Column 10, Line 22	Delete: "herein, and having respective" Insert: --herewith--

MAILING ADDRESS OF SENDER:

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